



Docket No.: SIG000116

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. 10/723,965
First Named Inventor: Fujio Takeda
Filed: 11/26/2003
Art Unit: 2838
Examiner: Dang, Robert Trong
Title: ESD protection circuit

Confirmation No. 9518

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action mailed December 20, 2005, please amend the above-identified application as follows and consider the following remarks. Please amend claims 1, 3, 4, 9, 11 and 12. Accordingly, claims 1-16 are still pending.

CERTIFICATE OF MAILING

37 C.F.R 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

4-20-2006
Date

William W. Kiehl
Signature